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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

(Established by Andhra Pradesh Act No.30 of 2008)

Kukatpally, Hyderabad – 500 085, Andhra Pradesh (India)

B. TECH. ELECTRONICS AND COMMUNICATION ENGINEERING

III YEAR II SEMESTER

Code	Subject	L	T/P/D	C
	Managerial Economics and Financial Analysis	4	-	4
	Open Elective: Human Values and Professional Ethics Disaster Management Intellectual Property Rights	4	-	4
	Digital Communications	4	-	4
	VLSI Design	4	-	4
	Microprocessors and Microcontrollers	4	-	4
	Digital Signal Processing	4	-	4
	Microprocessors and Microcontrollers Lab.	-	3	2
	Digital Signal Processing Lab.	-	3	2
	Total	**24**	**6**	**28**

Note: All End Examinations (Theory and Practical) are of three hours duration.
T-Tutorial **L – Theory** **P – Practical/Drawing** **C – Credits**

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
	4	-/-/-	4

(A60010) MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS

Objectives:

To enable the student to understand and appreciate, with a practical insight, the importance of certain basic issues governing the business operations namely: demand and supply, production function, cost analysis, markets, forms of business organisations, capital budgeting and financial accounting and financial analysis.

Unit I

Introduction & Demand Analysis: Definition, Nature and Scope of Managerial Economics. Demand Analysis: Demand Determinants, Law of Demand and its exceptions. Elasticity of Demand: Definition, Types, Measurement and Significance of Elasticity of Demand. Demand Forecasting, Factors governing demand forecasting, methods of demand forecasting.

Unit II

Production & Cost Analysis: Production Function – Isoquants and Isocosts, MRTS, Least Cost Combination of Inputs, Cobb-Douglas Production function, Laws of Returns, Internal and External Economies of Scale. Cost Analysis: Cost concepts. Break-even Analysis (BEA)-Determination of Break-Even Point (simple problems) - Managerial Significance.

Unit III

Markets & New Economic Environment: Types of competition and Markets, Features of Perfect competition, Monopoly and Monopolistic Competition. Price-Output Determination in case of Perfect Competition and Monopoly. Pricing: Objectives and Policies of Pricing. Methods of Pricing. Business: Features and evaluation of different forms of Business Organisation: Sole Proprietorship, Partnership, Joint Stock Company, Public Enterprises and their types, New Economic Environment: Changing Business Environment in Post-liberalization scenario.

Unit IV

Capital Budgeting: Capital and its significance, Types of Capital, Estimation of Fixed and Working capital requirements, Methods and sources of raising capital - Trading Forecast, Capital Budget, Cash Budget. Capital Budgeting: features of capital budgeting proposals, Methods of Capital Budgeting: Payback Method, Accounting Rate of Return (ARR) and Net Present Value Method (simple problems).

Unit V

Introduction to Financial Accounting & Financial Analysis: Accounting concepts and Conventions - Introduction IFRS - Double-Entry Book Keeping, Journal, Ledger, Trial Balance- Final Accounts (Trading Account, Profit and Loss Account and Balance Sheet with simple adjustments). Financial Analysis: Analysis and Interpretation of Liquidity Ratios, Activity Ratios, and Capital structure Ratios and Profitability ratios. Du Pont Chart.

TEXT BOOKS:

1. Varshney & Maheswari: Managerial Economics, Sultan Chand, 2009.
2. S.A. Siddiqui & A.S. Siddiqui, Managerial Economics and Financial Analysis, New Age international Publishers, Hyderabad 2013.
3. M. Kasi Reddy & Saraswathi, Managerial Economics and Financial Analysis, PHI New Delhi, 2012.

REFERENCES:

1. Ambrish Gupta, Financial Accounting for Management, Pearson Education, New Delhi.2012.
2. H. Craig Peterson & W. Cris Lewis, Managerial Economics, Pearson, 2012.
3. Lipsey & Chrystel, Economics, Oxford University Press, 2012
4. Domnick Salvatore: Managerial Economics in a Global Economy, Thomson, 2012.
5. Narayanaswamy: Financial Accounting—A Managerial Perspective, Pearson, 2012.
6. S.N.Maheswari & S.K. Maheswari, Financial Accounting, Vikas, 2012.
7. Truet and Truet: Managerial Economics: Analysis, Problems and Cases, Wiley, 2012.
8. Dwivedi: Managerial Economics, Vikas, 2012.
9. Shailaja & Usha : MEFA, University Press, 2012.
10. Aryasri: Managerial Economics and Financial Analysis, TMH, 2012.
11. Vijay Kumar & Appa Rao, Managerial Economics & Financial Analysis, Cengage 2011.
12. J. V. Prabhakar Rao & P.V. Rao, Managerial Economics & Financial Analysis, Maruthi Publishers, 2011.

Outcomes:

At the end of the course, the student will

- Understand the market dynamics namely, demand and supply, demand forecasting, elasticity of demand and supply, pricing methods and pricing in different market structures.

- Gain an insight into how production function is carried out to achieve least cost combination of inputs and cost analysis
- Develop an understanding of
- Analyse how capital budgeting decisions are carried out
- Understand the framework for both manual and computerised accounting process
- Know how to analyse and interpret the financial statements through ratio analysis.

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(A60018) HUMAN VALUES AND PROFESSIONAL ETHICS

(Open Elective)

Objectives : This introductory course input is intended

- To help the students appreciate the essential complementarity between 'VALUES' and 'SKILLS' to ensure sustained happiness and prosperity which are the core aspirations of all human beings.
- To facilitate the development of a Holistic perspective among students towards life, profession and happiness, based on a correct understanding of the Human reality and the rest of Existence. Such a holistic perspective forms the basis of Value based living in a natural way.
- To highlight plausible implications of such a Holistic understanding in terms of ethical human conduct, trustful and mutually satisfying human behavior and mutually enriching interaction with Nature.

Unit I:

Course Introduction - Need, Basic Guidelines, Content and Process for Value Education: Understanding the need, basic guidelines, content and process for Value Education. Self Exploration–what is it? - its content and process; 'Natural Acceptance' and Experiential Validation- as the mechanism for self exploration. Continuous Happiness and Prosperity- A look at basic Human Aspirations. Right understanding, Relationship and Physical Facilities- the basic requirements for fulfillment of aspirations of every human being with their correct priority. Understanding Happiness and Prosperity correctly- A critical appraisal of the current scenario. Method to fulfill the above human aspirations: understanding and living in harmony at various levels.

Unit II:

Understanding Harmony in the Human Being - Harmony in Myself! : Understanding human being as a co-existence of the sentient 'I' and the material 'Body'. Understanding the needs of Self ('I') and 'Body' - Sukh and Suvidha. Understanding the Body as an instrument of 'I' (I being the doer, seer and enjoyer). Understanding the characteristics and activities of 'I' and harmony in 'I'. Understanding the harmony of I with the Body: Sanyam and Swasthya; correct appraisal of Physical needs, meaning of Prosperity in detail. Programs to ensure Sanyam and Swasthya.

Unit III:

Understanding Harmony in the Family and Society- Harmony in Human - Human Relationship : Understanding harmony in the Family- the basic unit of human interaction. Understanding values in human-human relationship; meaning of Nyaya and program for its fulfillment to ensure Ubhay-tripti; Trust (Vishwas) and Respect (Samman) as the foundational values of relationship. Understanding the meaning of Vishwas; Difference between intention and competence. Understanding the meaning of Samman, Difference between respect and differentiation; the other salient values in relationship. Understanding the harmony in the society (society being an extension of family): Samadhan, Samridhi, Abhay, Sah-astitva as comprehensive Human Goals. Visualizing a universal harmonious order in society- Undivided Society (Akhand Samaj), Universal Order (Sarvabhaum Vyawastha)- from family to world family!

Unit IV:

Understanding Harmony in the Nature and Existence - Whole existence as Co-existence : Understanding the harmony in the Nature. Interconnectedness and mutual fulfillment among the four orders of nature-recyclability and self-regulation in nature. Understanding Existence as Co-existence (Sah-astitva) of mutually interacting units in all-pervasive space. Holistic perception of harmony at all levels of existence.

Unit V:

Implications of the above Holistic Understanding of Harmony on Professional Ethics : Natural acceptance of human values. Definitiveness of Ethical Human Conduct. Basis for Humanistic Education, Humanistic Constitution and Humanistic Universal Order. Competence in professional ethics:

a) Ability to utilize the professional competence for augmenting universal human order,

b) Ability to identify the scope and characteristics of people-friendly and eco-friendly production systems,

c) Ability to identify and develop appropriate technologies and management patterns for above production systems.

Case studies of typical holistic technologies, management models and production systems. Strategy for transition from the present state to Universal Human Order:

a) At the level of individual: as socially and ecologically responsible engineers, technologists and managers

b) At the level of society: as mutually enriching institutions and organizations

TEXT BOOKS

1. R R Gaur, R Sangal, G P Bagaria, 2009, A Foundation Course in Human Values and Professional Ethics.
2. Prof. KV Subba Raju, 2013, Success Secrets for Engineering Students, Smart Student Publications,3rd Edition.

REFERENCE BOOKS

1. Ivan Illich, 1974, Energy & Equity, The Trinity Press, Worcester, and HarperCollins, USA
2. E.F. Schumacher, 1973, Small is Beautiful: a study of economics as if people mattered, Blond & Briggs, Britain.
3. A Nagraj, 1998, Jeevan Vidya ek Parichay, Divya Path Sansthan, Amarkantak.
4. Sussan George, 1976, How the Other Half Dies, Penguin Press. Reprinted 1986, 1991
5. PL Dhar, RR Gaur, 1990, Science and Humanism, Commonwealth Purblishers.
6. A.N. Tripathy, 2003, Human Values, New Age International Publishers.
7. Subhas Palekar, 2000, How to practice Natural Farming, Pracheen(Vaidik) Krishi Tantra Shodh, Amravati.
8. Donella H. Meadows, Dennis L. Meadows, Jorgen Randers, William W. Behrens III, 1972, Limits to Growth – Club of Rome's report, Universe Books.
9. E G Seebauer & Robert L. Berry, 2000, Fundamentals of Ethics for Scientists & Engineers, Oxford University Press
10. M Govindrajran, S Natrajan & V.S. Senthil Kumar, Engineering Ethichs (including Human Values), Eastern Economy Edition, Prentice Hall of India Ltd.

Relevant CDs, Movies, Documentaries & Other Literature:

1. Value Education website, http://www.uptu.ac.in
2. Story of Stuff, http://www.storyofstuff.com
3. Al Gore, An Inconvenient Truth, Paramount Classics, USA
4. Charlie Chaplin, Modern Times, United Artists, USA
5. IIT Delhi, Modern Technology – the Untold Story

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
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(A60117) DISASTER MANAGEMENT

(Open Elective)

Unit-I

Environmental Hazards & Disasters: Meaning of Environmental hazards, Environmental Disasters and Environmental stress. Concept of Environmental Hazards, Environmental stress & Environmental Disasters. Different approaches & relation with human Ecology - Landscape Approach - Ecosystem Approach - Perception approach - Human ecology & its application in geographical researches.

Unit –II

Types of Environmental hazards & Disasters: Natural hazards and Disasters - Man induced hazards & Disasters - Natural Hazards- Planetary Hazards/ Disasters - Extra Planetary Hazards/ disasters - Planetary Hazards- Endogenous Hazards - Exogenous Hazards –

Unit –III

Endogenous Hazards - Volcanic Eruption – Earthquakes – Landslides - Volcanic Hazards/ Disasters - Causes and distribution of Volcanoes - Hazardous effects of volcanic eruptions - Environmental impacts of volcanic eruptions - Earthquake Hazards/ disasters - Causes of Earthquakes - Distribution of earthquakes - Hazardous effects of - earthquakes - - Earthquake Hazards in India - - Human adjustment, perception & mitigation of earthquake.

Unit –IV

Exogenous hazards/ disasters - Infrequent events– Cumulative atmospheric hazards/ disasters

Infrequent events: Cyclones – Lightning – Hailstorms

Cyclones: Tropical cyclones & Local storms - Destruction by tropical cyclones & local storms (causes , distribution human adjustment, perception & mitigation) Cumulative atmospheric hazards/ disasters : - Floods- Droughts- Cold waves- Heat waves Floods:- Causes of floods- Flood hazards India- Flood control measures (Human adjustment, perception & mitigation) Droughts:- Impacts of droughts- Drought hazards in India- Drought control measures- Extra Palnetary Hazards/ Disasters- Man induced Hazards / Disasters- Physical hazards/ Disasters-Soil Erosion

Soil Erosion:-- Mechanics & forms of Soil Erosion- Factors & causes of Soil Erosion- Conservation measures of Soil Erosion

Chemical hazards/ disasters:-- Release of toxic chemicals, nuclear explosion- Sedimentation processes Sedimentation processes:- Global Sedimentation problems- Regional Sedimentation problems- Sedimentation & Environmental problems- Corrective measures of Erosion & Sedimentation

Biological hazards/ disasters:- Population Explosion.

Unit –V

Emerging approaches in Disaster Management- Three Stages

1. Pre- disaster stage (preparedness)
2. Emergency Stage
3. Post Disaster stage-Rehabilitation

TEXT BOOKS:

1. Disaster Mitigation: Experiences And Reflections by Pardeep Sahni
2. Natural Hazards & Disasters by Donald Hyndman & David Hyndman – Cengage Learning

REFERENCES

1. R.B.Singh (Ed) Environmental Geography, Heritage Publishers New Delhi,1990
2. Savinder Singh Environmental Geography, Prayag Pustak Bhawan, 1997
3. Kates,B.I & White, G.F The Environment as Hazards, oxford, New York, 1978
4. R.B. Singh (Ed) Disaster Management, Rawat Publication, New Delhi, 2000
5. H.K. Gupta (Ed) Disaster Management, Universiters Press, India, 2003
6. R.B. Singh, Space Technology for Disaster Mitigation in India (INCED), University of Tokyo, 1994
7. Dr. Satender , Disaster Management t in Hills, Concept Publishing Co., New Delhi, 2003
8. A.S. Arya Action Plan For Earthquake,Disaster, Mitigation in V.K. Sharma (Ed) Disaster Management IIPA Publication New Delhi, 1994
9. R.K. Bhandani An overview on Natural & Man made Disaster & their Reduction,CSIR, New Delhi
10. M.C. Gupta Manuals on Natural Disaster management in India, National Centre for Disaster Management, IIPA, New Delhi, 2001

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(A60017) INTELLECTUAL PROPERTY RIGHTS

(Open Elective)

UNIT – I

Introduction to Intellectual property: Introduction, types of intellectual property, international organizations, agencies and treaties, importance of intellectual property rights.

UNIT – II

Trade Marks : Purpose and function of trade marks, acquisition of trade mark rights, protectable matter, selecting and evaluating trade mark, trade mark registration processes.

UNIT – III

Law of copy rights : Fundamental of copy right law, originality of material, rights of reproduction, rights to perform the work publicly, copy right ownership issues, copy right registration, notice of copy right, international copy right law.

Law of patents : Foundation of patent law, patent searching process, ownership rights and transfer

UNIT – IV

Trade Secrets : Trade secrete law, determination of trade secrete status, liability for misappropriations of trade secrets, protection for submission, trade secrete litigation.

Unfair competition : Misappropriation right of publicity, False advertising.

UNIT – V

New development of intellectual property: new developments in trade mark law ; copy right law, patent law, intellectual property audits.

International overview on intellectual property, international – trade mark law, copy right law, international patent law, international development in trade secrets law.

TEXT BOOKS & REFERENCES:

1. Intellectual property right, Deborah. E. Bouchoux, cengage learing.
2. Intellectual property right – Unleashing the knowledge economy, prabuddha ganguli, Tate Mc Graw Hill Publishing company ltd.,

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(A60420) DIGITAL COMMUNICATIONS

Course Objectives:

The objectives are:

- To understand different digital modulation techniques such as PCM, DM and various shift keying techniques.
- Understand the concepts of different digital modulation techniques.
- To study about different error detecting and error correcting codes like block codes, cyclic codes and convolution codes
- To study the advantages of spread spectrum techniques and performance of spread spectrum, PN codes in jamming, noise etc.

UNIT -I:

Elements of Digital Communication Systems: Advantages of Digital Communication Systems, Bandwidth-S/N Tradeoff, Hartley Shanon Law and Sampling Theorem.

Pulse Code Modulation: PCM Generation and Reconstruction, Quantization Noise, Non Uniform Quantization and Companding, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT -II:

Digital Modulation Techniques: Introduction, ASK,ASK Modulator, Coherent ASK Detector, Non-Coherent ASK Detector, FSK, Bandwidth and Frequency Spectrum FSK, Non Coherent FSK Detector, Coherent FSK Detector, FSK Detection using PLL, BPSK, Coherent PSK Detection, QPSK, Differential PSK.

UNIT -III:

Baseband Transmission and Optimal Reception of Digital Signal: Pulse Shaping for Optimum Transmissions, A Baseband Signal Receiver, Probability of Error, Optimum Receiver, Optimal of Coherent Reception, Signal Space Representation and Probability of Error and Eye Diagrams for ASK, PSK, FSK, Cross Talk.

Information Theory: Information and entropy, conditional entropy and redundancy, Shannon Fano coding, Mutual Information, Information loss due to noise, source codings – Huffman Code, variable length coding, Source coding to Increase average Information per bit, Lossy source coding.

UNIT -IV:

Error Control Codes

Linear Block Codes: Matrix Description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes.

Cyclic Codes: Algebraic Structure, Encoding, Syndrome Calculation, Decoding.

Convolution Codes: Encoding, Decoding using State, Tree and Trellis Diagrams, Decoding using Viterbi Algorithm, Comparison of Error Rates in Coded and Uncoded Transmission.

UNIT -V:

Spread Spectrum Modulation: Use of Spread Spectrum, Direct Sequence Spread Spectrum (DSSS), Code Division Multiple Access, Ranging using DSSS, Frequency Hopping Spread Spectrum, PN - Sequences: Generation and Characteristics, Synchronization in Spread Spectrum Systems

TEXT BOOKS:

1. Principles of Communication Systems - Herbert Taub, Donald L Schiling, Goutam Saha, 3rd Edition, Mcgraw-Hill, 2008.
2. Digital and Analog Communication Systems – Sam Shanmugam, John Wiley, 2005.
3. Digital Communications – John G. Proakis , Masoud Salehi – 5th Edition, Mcgraw-Hill, 2008.

REFERENCE BOOKS:

1. Digital Communication – Simon Haykin, John Wiley, 2005.
2. Digital Communications – Ian A. Glover, Peter M. Grant, 2nd Edition, Pearson Edu., 2008.
3. Communication Systems – B.P. Lathi, BS Publication, 2006.
4. A First course in Digital Communications -Nguyen, Shewedyh, Cambride.
5. Digital Communication- Theory, Techniques, and Applications _ R. N. Mutagi, 2nd Ed. 2013.

Course Outcomes:

At the end of the course, the student will be able to:

- Understand basic components of digital communication systems.
- Design optimum receivers for digital modulation techniques.
- Analyze the error performance of digital modulation techniques.
- Know about different error detecting and error correcting codes like block codes, cyclic codes and convolution codes.
- Understand the advantages of spread spectrum techniques and performance of spread spectrum, PN codes in jamming, noise etc.

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(A60432) VLSI DESIGN

Course Objectives:

The objectives of the course are to:

- Give exposure to different steps involved in the fabrication of ICs using MOS transistor, CMOS/BICMOS transistors and passive components
- Explain electrical properties of MOS and BiCMOS devices to analyze the behavior of inverters designed with various loads.
- Give exposure to the design rules to be followed to draw the layout of any logic circuit.
- Provide concept to design different types of logic gates using CMOS inverter and analyze their transfer characteristics.
- Provide design concepts to design building blocks of data path of any system using gates.
- Understand basic programmable logic devices and testing of CMOS circuits.

UNIT –I:

Introduction: Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS

Basic Electrical Properties: Basic Electrical Properties of MOS and BiCMOS Circuits: Ids-Vds relationships, MOS transistor threshold Voltage, gm, gds, Figure of merit ?o; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT -II:

VLSI Circuit Design Processes: VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2 µm CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits.

UNIT –III:

Gate Level Design: Logic Gates and Other complex gates, Switch logic, Alternate gate circuits, Time delays, Driving large capacitive loads, Wiring capacitance, Fan – in, Fan – out, Choice of layers.

UNIT -IV:

Data Path Subsystems: Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters.

Array Subsystems: SRAM, DRAM, ROM, Serial Access Memories.

UNIT -V:

Programmable Logic Devices: PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach, Parameters influencing low power design.

CMOS Testing: CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chip level Test Techniques.

TEXT BOOKS:

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Dougles and A. Pucknell, PHI, 2005 Edition
2. CMOS VLSI Design – A Circuits and Systems Perspective, Neil H. E Weste, David Harris, Ayan Banerjee, 3rd Ed, Pearson, 2009.
3. VLSI Design – M. Michael Vai, 2001, CRC Press.

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRC Press, 2011
2. CMOS logic circuit Design - John .P. Uyemura, Springer, 2007.
3. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.
4. VLSI Design- K .Lal Kishore, V. S. V. Prabhakar, I.K International, 2009.
5. Introduction to VLSI – Mead & Convey, BS Publications, 2010.

Course Outcomes:

Upon successfully completing the course, the student should be able to:

- Acquire qualitative knowledge about the fabrication process of integrated circuit using MOS transistors.
- Choose an appropriate inverter depending on specifications required for a circuit
- Draw the layout of any logic circuit which helps to understand and estimate parasitics of any logic circuit
- Design different types of logic gates using CMOS inverter and analyze their transfer characteristics
- Provide design concepts required to design building blocks of data path using gates.
- Design simple memories using MOS transistors and can understand Design of large memories.
- design simple logic circuit using PLA, PAL, FPGA and CPLD.
- Understand different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system

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(A60430) MICROPROCESSORS AND MICROCONTROLLERS

Course Objective:

The course objectives are:

- To develop an in-depth understanding of the operation of microprocessors and microcontrollers, machine language programming & interfacing techniques.

UNIT -I:

8086 Architecture: 8086 Architecture-Functional diagram, Register Organization, Memory Segmentation, Programming Model, Memory addresses, Physical Memory Organization, Architecture of 8086, Signal descriptions of 8086- Common Function Signals, Timing diagrams, Interrupts of 8086.

UNIT -II:

Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, Simple Programs involving Logical, Branch and Call Instructions, Sorting, Evaluating Arithmetic Expressions, String Manipulations.

UNIT -III:

I/O Interface: 8255 PPI, Various Modes of Operation and Interfacing to 8086, Interfacing Keyboard, Display, D/A and A/D Converter.

Interfacing with advanced devices: Memory Interfacing to 8086, Interrupt Structure of 8086, Vector Interrupt Table, Interrupt Service Routine.

Communication Interface: Serial Communication Standards, Serial Data Transfer Schemes, 8251 USART Architecture and Interfacing.

UNIT -IV:

Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051, Simple Programs

UNIT -V:

8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

TEXT BOOKS:

1. D. V. Hall, Microprocessors and Interfacing, TMGH, 2nd Edition 2006.

2. Kenneth. J. Ayala, The 8051 Microcontroller , 3rd Ed., Cengage Learning.

REFERENCE BOOKS:

1. Advanced Microprocessors and Peripherals – A. K. Ray and K.M. Bhurchandani, TMH, 2nd Edition 2006.
2. The 8051Microcontrollers, Architecture and Programming and Applications -K.Uma Rao, Andhe Pallavi, Pearson, 2009.
3. Micro Computer System 8086/8088 Family Architecture, Programming and Design - Liu and GA Gibson, PHI, 2nd Ed.
4. Microcontrollers and Application - Ajay. V. Deshmukh, TMGH, 2005.
5. The 8085 Microprocessor: Architecture, programming and Interfacing – K.Uday Kumar, B.S.Umashankar, 2008, Pearson

Course Outcome:

Upon completion of the course:

- The student will learn the internal organization of popular 8086/8051 microprocessors/microcontrollers.
- The student will learn hardware and software interaction and integration.
- The students will learn the design of microprocessors/ microcontrollers-based systems.

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(A60421) DIGITAL SIGNAL PROCESSING

Objectives:

This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete-time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

UNIT -I:

Introduction: Introduction to Digital Signal Processing: Discrete Time Signals & Sequences, Linear Shift Invariant Systems, Stability, and Causality, Linear Constant Coefficient Difference Equations, Frequency Domain Representation of Discrete Time Signals and Systems

Realization of Digital Filters: Applications of Z – Transforms, Solution of Difference Equations of Digital Filters, System Function, Stability Criterion, Frequency Response of Stable Systems, Realization of Digital Filters – Direct, Canonic, Cascade and Parallel Forms.

UNIT -II:

Discrete Fourier series: DFS Representation of Periodic Sequences, Properties of Discrete Fourier Series, Discrete Fourier Transforms: Properties of DFT, Linear Convolution of Sequences using DFT, Computation of DFT: Over-Lap Add Method, Over-Lap Save Method, Relation between DTFT, DFS, DFT and Z-Transform.

Fast Fourier Transforms: Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

UNIT-III:

IIR Digital Filters: Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

UNIT-IV:

FIR Digital Filters: Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

UNIT-V:

Multirate Digital Signal Processing: Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion.

Finite Word Length Effects: Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round Off Noise, Methods to Prevent Overflow, Trade Off Between Round Off and Overflow Noise, Dead Band Effects.

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007.
2. Discrete Time Signal Processing – A. V. Oppenheim and R.W. Schaffer, PHI, 2009
3. Fundamentals of Digital Signal Processing – Loney Ludeman, John Wiley, 2009

REFERENCE BOOKS:

1. Digital Signal Processing – Fundamentals and Applications – Li Tan, Elsevier, 2008
2. Fundamentals of Digital Signal Processing using MATLAB – Robert J. Schilling, Sandra L. Harris, Thomson, 2007
3. Digital Signal Processing – S.Salivahanan, A.Vallavaraj and C.Gnanapriya, TMH, 2009
4. Discrete Systems and Digital Signal Processing with MATLAB – Taan S. ElAli, CRC press, 2009.
5. Digital Signal Processing - A Practical approach, Emmanuel C. Ifeachor and Barrie W. Jervis, 2nd Edition, Pearson Education, 2009.
6. Digital Signal Processing - Nagoor Khani, TMG, 2012

Course Outcomes:

On completion of this subject, the student should be able to:

- Perform time, frequency and Z -transform analysis on signals and systems.
- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of roundoff errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
	-	-/3/-	2

(A60494) MICROPROCESSORS AND MICROCONTROLLERS LAB

Note: Minimum of 12 experiments are to be conducted.

The Following programs/experiments are to be written for assembler and to be executed the same with 8086 and 8051 kits.

List of Experiments:

1 Programs for 16 bit arithmetic operations for 8086 (using Various Addressing Modes).
2 Program for sorting an array for 8086.
3 Program for searching for a number or character in a string for 8086.
4 Program for string manipulations for 8086.
5 Program for digital clock design using 8086.
6 Interfacing ADC and DAC to 8086.
7 Parallel communication between two microprocessors using 8255.
8 Serial communication between two microprocessor kits using 8251.
9 Interfacing to 8086 and programming to control stepper motor.
10 Programming using arithmetic, logical and bit manipulation instructions of 8051.
11 Program and verify Timer/ Counter in 8051.
12 Program and verify Interrupt handling in 8051
13 UART Operation in 8051.
14 Communication between 8051 kit and PC.
15 Interfacing LCD to 8051.
16 Interfacing Matrix/ Keyboard to 8051.
17 Data Transfer from Peripheral to Memory through DMA controller 8237 / 8257.

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III Year B.Tech. ECE-II Sem	L	T/P/D	C
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(A60493) DIGITAL SIGNAL PROCESSING LAB

Note:

- Minimum of 12 experiments are to be conducted.
- The programs shall be implemented in software (Using MATLAB / Lab view / C programming/OCTAVE Equivalent) and hardware (Using TI / Analog devices / Motorola / Equivalent DSP processors).

List of Experiments:

1 Generation of Sinusoidal waveform / signal based on recursive difference equations
2 To find DFT / IDFT of given DT signal
3 To find frequency response of a given system given in (Transfer Function/ Differential equation form).
4 Implementation of FFT of given sequence
5 Determination of Power Spectrum of a given signal(s).
6 Implementation of LP FIR filter for a given sequence
7 Implementation of HP FIR filter for a given sequence
8 Implementation of LP IIR filter for a given sequence
9 Implementation of HP IIR filter for a given sequence
10 Generation of Sinusoidal signal through filtering
11 Generation of DTMF signals
12 Implementation of Decimation Process
13 Implementation of Interpolation Process
14 Implementation of I/D sampling rate converters
15 Audio application such as to plot a time and frequency display of microphone plus a cosine using DSP. Read a .wav file and match with their respective spectrograms.
16 Noise removal: Add noise above 3 KHz and then remove, interference suppression using 400 Hz tone.
17 Impulse response of first order and second order systems.